Stic-AmSud workshop on network performance and dependability

Organized by the Stic AmSud "AMMA" project (Chile, France, Uruguay)

with
- Inria associated team "MANAP" with UTFSM, Valparaíso, Chile,
- Inria-Chile CIRIC project,
- Engineering School of UDELAR,

and support from the Embassy of France in Uruguay.

Salón de postgrados del Instituto de computación, 5º piso
Martes 17 de 9:00 a 12:45, y **miércoles 18, de 9:00 a 11:50** y de **14:00 a 16:00**

Program

Tuesday 17

09:00 - 09:10	Opening, by G. Rubino, Inria, France, AMMA and MANAP leader
09:10 - 09:50	"*Solving Constrained Shortest Path Problems with the Pulse Algorithm*", by Andrés Medaglia, Univ. de los Andes, Colombia
09:50 - 10:30	"*A New Recurrent and Analytical Solution to the M/E/1 Queue*", by José Manuel Martínez, UTFSM, Chile
10:30 - 11:10	"*Sparse Vector Implementation of the Uniformization Technique*", by Reinaldo Vallejos, UTFSM, Chile
11:10 - 11:25	--- coffee break ---
11:25 - 12:05	"*Fluid limits in the Machine Repairman Model*", by Laura Aspirot, IMERL, UDELAR, Uruguay
12:05 - 12:45 –	"*Zero-variance approach and learning methods in Monte Carlo*" (title to be confirmed), by Bruno Tuffin, Inria, France

Wednesday 18

09:00 -10:00	"*Reliability and Availability Models in Practice*", by Kishor Trivedi, Duke University, USA
10:00 -10:40	"*Ant-Workers in a Cooperative Network Game*", by Pablo Romero, IMERL, UDELAR, Uruguay
10:40 -11:00	--- coffee break ---
11:00 -11:40	"*Should I Tweet or should I go*?", by Gonzalo Perera, IMERL, UDELAR
11:40 -11:50	Closing of the presentations' part, by H. Cancela, Engineering School, UDELAR, Uruguay
14:00 – 16:00	"*Diameter-constrained network reliability: properties and calculation methods*", PhD dissertation, by Pablo Sartor